IN THE SPECIFICATION:

Page 5, Line 1, after the word "but," please insert the word --is--.

Page 8, line 26, please change "experiences" to --experience--.

Page 11, line 21, please delete --in--.

IN THE CLAIMS:

Please cancel claim 19 without prejudice or disclaimer of subject matter.

Please amend the claims as follows:

Sub E1

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1. (Amended) A circuit comprising:

a first circuit having a first input and a first output, wherein said first output [being] includes a function of a signal at said first input [plus a] and also includes a first noise component resulting from noise experienced by said first circuit;

a second circuit, [identical to and] located [proximate] proximal to said first circuit[,] and having a second input and a second output, wherein said second output includes a function of a signal at said second input and also includes a second noise component resulting from noise experienced by said second circuit, and wherein the second noise component is approximately equal to the first noise component;

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[said second input set to cause said second output to equal said noise component of said first output;] and

a subtractor circuit connected to said first circuit and to said second circuit to subtract said second output from said first output.

3. (Amended) A circuit according to claim 1 wherein said subtractor circuit further comprises a halving circuit which reduces a signal by one-half its amplitude.

4. (Amended) A circuit comprising:

a first circuit having a first input and a first output, [said first output being a function of said first input plus noise;] wherein said first output includes a function of a signal at said first input and also includes a first noise component resulting from noise experienced by said first circuit;

[a second circuit, identical to said first circuit, and] a second circuit having a second input and a second output, wherein said second output includes an input signal component which is a function of a signal at said second input and also includes a second noise component resulting from noise experienced by said second circuit, wherein the input signal component is a null output, and wherein the second noise component is approximately equal to the first noise component;

[said second input designed to cause said second circuit to produce, as said second output, said noise only;] and

a third circuit having a third input connected to said first output[,] and a fourth input connected to said second output to subtract said second output from said first output.

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7. (Amended) A circuit comprising:

a first circuit having a first input and a first output, wherein said first output [being] includes a function of a signal at said first input and also includes a noise component resulting from noise experienced by [plus noise;

a second circuit, identical to] said first circuit[, and];

a second circuit having a second input and a second output;

[said second input, being an inverse of said first input, causing said second circuit to produce, as said second output, an inverse function of said first circuit plus noise] a signal supplying circuit suppling to the second input a signal an inverse of the signal at the first input; and

a third circuit having a third input connected to said first output and a fourth input connected to said second output, and [combining] subtracting said second output from said first output.

8. (Amended) A circuit according to claim 7 wherein said third circuit further comprises a halving circuit which reduces a signal by one-half its amplitude.

10. (Amended) A circuit according to claim 9, wherein said first circuit, said second circuit, said [operator] third circuit, and said digital circuit are on a single integrated circuit chip.

11. (Twice Amended) An integrated circuit chip (IC) [circuit] comprising:

[a digital circuit;]

a plurality of analog circuits, each proximal to [said digital circuit and] to each other, and each of said plurality of analog circuits producing an output signal which includes a function of an input signal and also includes a noise component resulting from noise experienced by said plurality of analog circuits;

a noise separator circuit, proximal to said plurality of analog circuits, and producing a noise signal based on noise experienced by said noise separator circuit, wherein the noise signal is approximately equal to the noise component of the output signal output by each of the plurality of analog circuits; and

a noise canceling circuit[, processing said outputs signal] which processes said output signals with said noise signal to reduce the noise component of the output signal output by each of the plurality of analog circuits.

13. (Amended) An IC according to claim 11 wherein said noise canceling circuit further comprises a halving circuit which reduces a signal by one-half its amplitude.

14. (Amended) A noise cancellation method comprising the steps:

supplying a first signal to a first circuit;

reading a first output from said first circuit;

supplying a [null] signal to a second circuit which results in a null output from the second circuit, wherein said second circuit is located proximal to said first circuit;

reading a second output from said second circuit;

combining said first output with said second output to produce a combinational output,

wherein a noise component of the first output due to noise experienced by said first circuit is approximately equal to a noise component of the second circuit due to noise experienced by said second circuit.

17. (Amended) A method according to claim 14 wherein said step of combination comprises the step of adding said second output to said first output to produce an added output.

18. (Amended) A method according to claim 17 wherein said step of combination further comprises the step of [halving] reducing said added output signal by one-half its amplitude.

Please add claims 20 to 29 as follows:

--20. A circuit according to claim 1, wherein said second circuit is identical to said first circuit.

21. A circuit according to claim 1, wherein the noise experienced by said first circuit and said second circuit is electromagnetic environmental noise.

22. A circuit according to claim 1 wherein said second circuit is located close enough to said first circuit so that said second circuit experiences approximately the same noise as said first circuit.

23. A circuit according to claim 1, wherein said ~~third~~ subtractor circuit is digital.

24. A circuit according to claim 1, wherein said ~~third~~ subtractor circuit is analog.

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25. A circuit according to claim 4, wherein said second circuit is identical to said first circuit.

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26. A circuit according to claim 4, wherein the noise experienced by said first circuit and said second circuit is electromagnetic environmental noise.

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27. A circuit according to claim 7, wherein said second circuit is identical to said first circuit.

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28. A circuit according to claim 11, wherein the noise experienced by said plurality of analog circuits and said noise separator circuit is electromagnetic environmental noise.

24 21
29. A circuit according to claim 14, wherein the noise experienced by said first circuit and said second circuit is electromagnetic environmental noise.--

IN THE DRAWINGS:

Please approve the accompanying Request for Approval of Drawing Changes.